

Package, Tape-and-Reel and Pad Layout Specifications

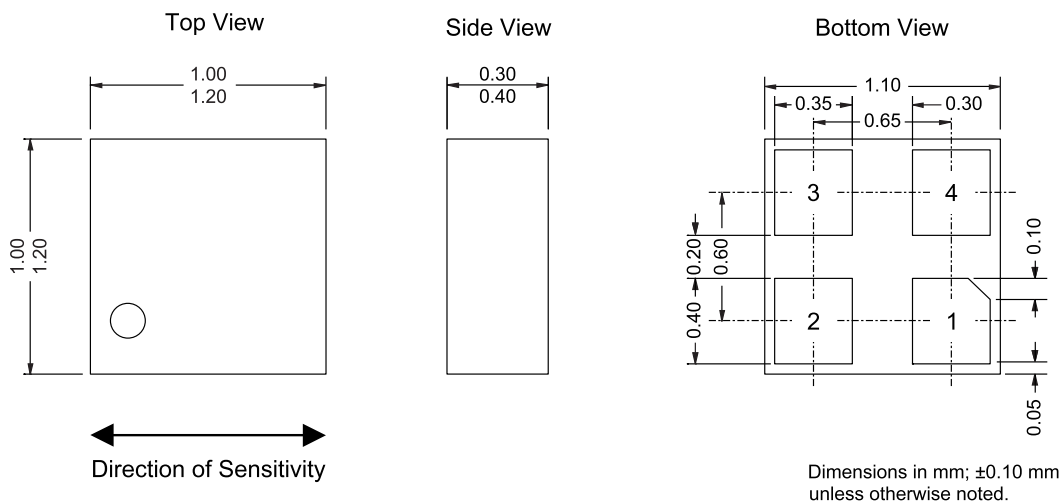
Best-in-Class Miniaturization

Package Drawings

All NVE packages are compliant with JEDEC standards, but some also meet creepage requirements unique to isolators.

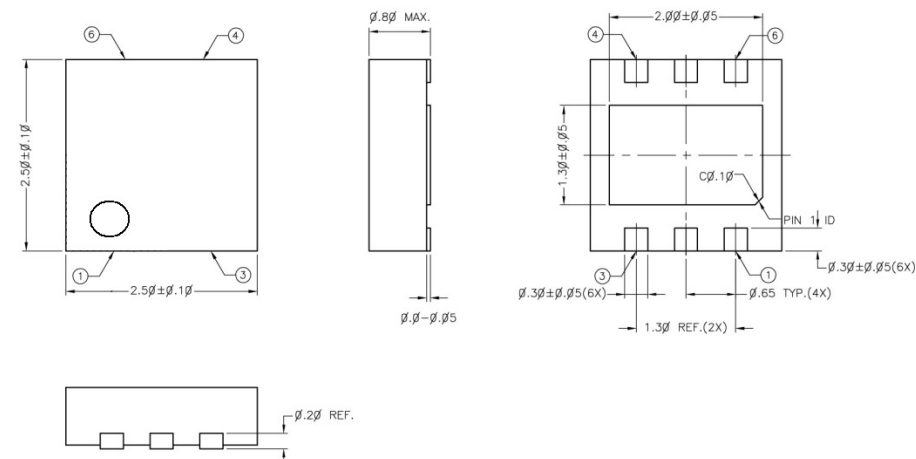
DFN4 (ULLGA)

Dimensions in mm; scale = approx. 25X



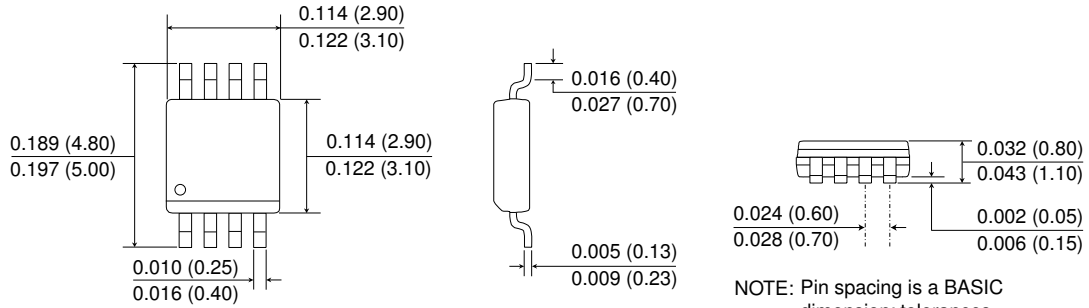
DFN6

Dimensions in mm; scale = approx. 10X



MSOP8

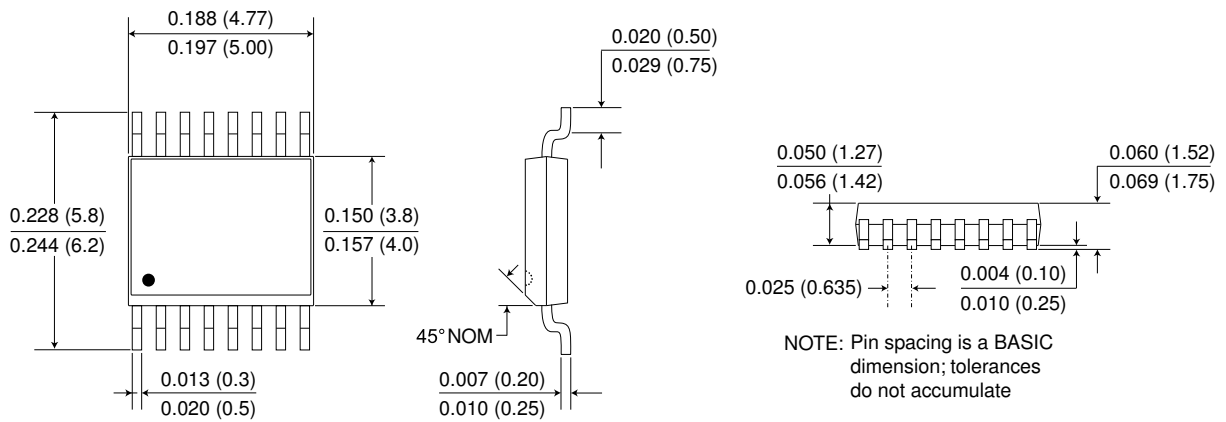
Dimensions in inches (mm); scale = approx. 5X



NOTE: Pin spacing is a BASIC dimension; tolerances do not accumulate

QSOP16

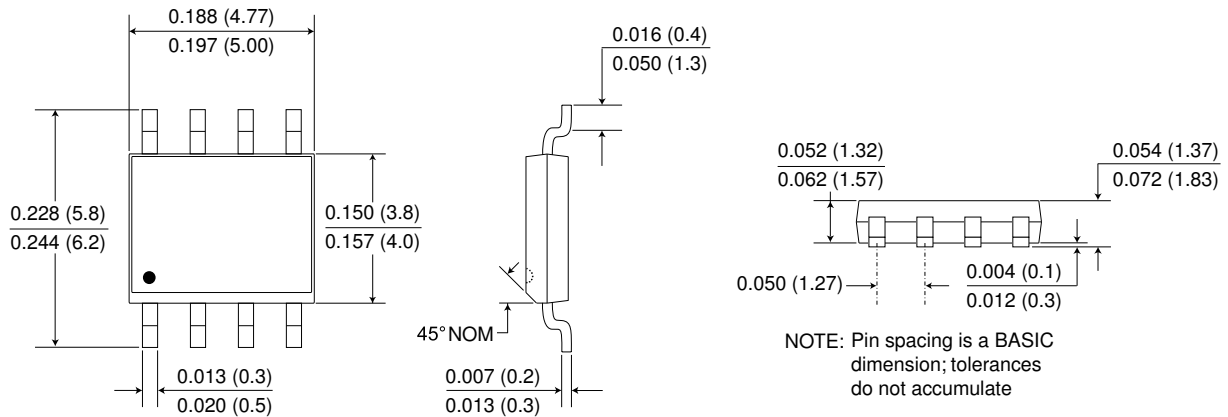
Dimensions in inches (mm); scale = approx. 5X



NOTE: Pin spacing is a BASIC dimension; tolerances do not accumulate

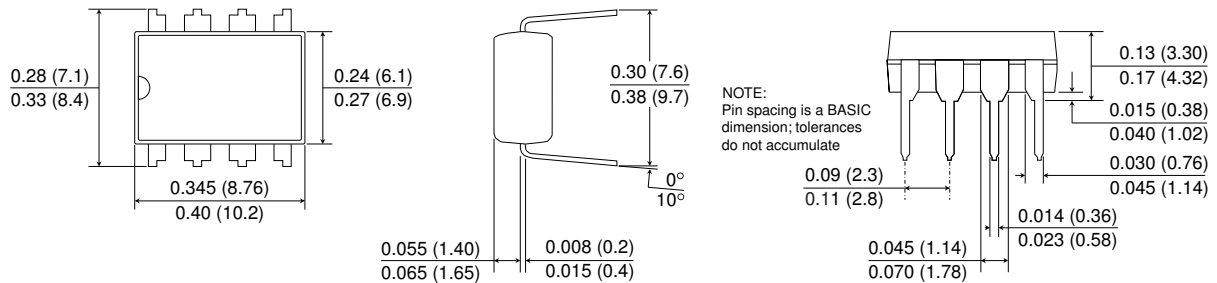
SOIC8 Package

Dimensions in inches (mm); scale = approx. 5X



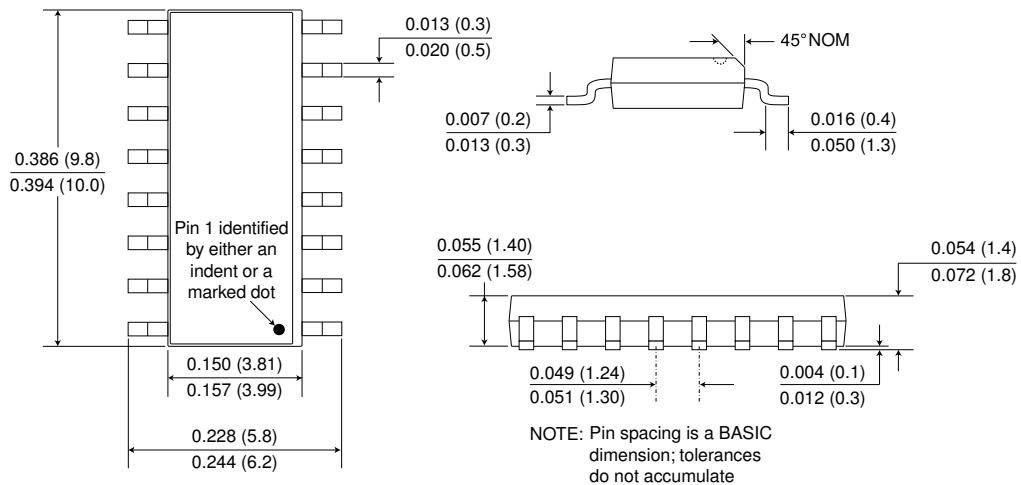
PDIP8

Dimensions in inches (mm); scale = approx. 2.5X



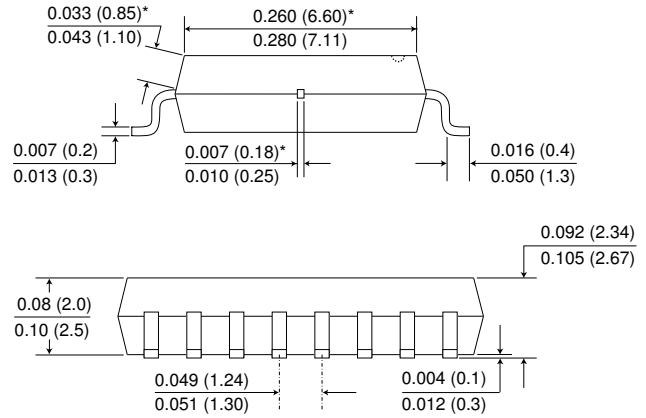
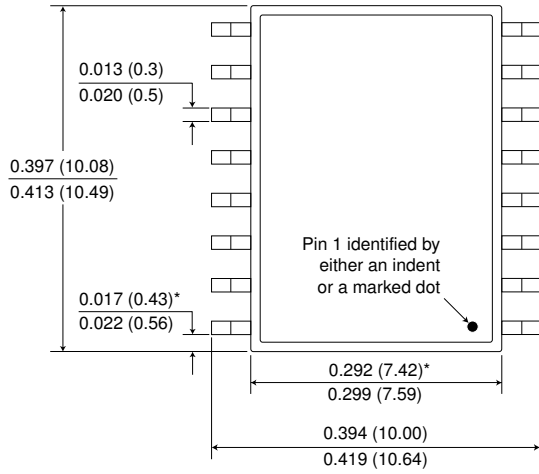
0.15" SOIC16 (-3 suffix)

Dimensions in inches (mm); scale = approx. 5X



0.3" SOIC16 Package (no suffix)

Dimensions in inches (mm); scale = approx. 5X

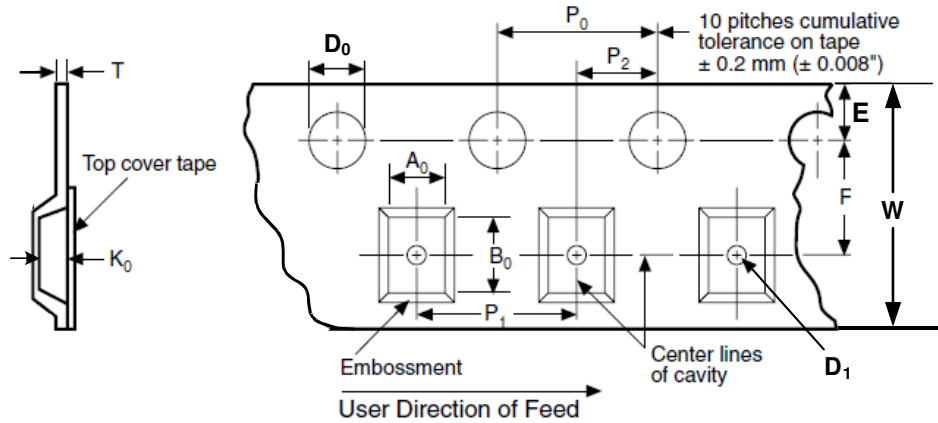


NOTE: Pin spacing is a BASIC dimension; tolerances do not accumulate

*Specified for True 8™ package to guarantee 8 mm creepage per IEC 60601.

Tape-and-Reel Specifications

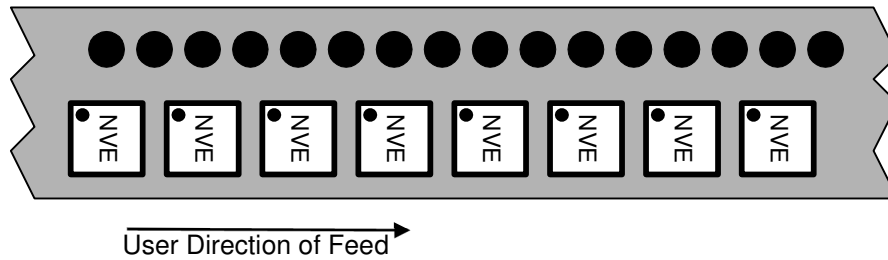
Tape-and-Reel Dimensions



Dimensions in millimeters

Tape Width	D ₀	E ₁	F	P ₀	P ₁	P ₂	W	T
8 mm	1.5±0.10	1.75±0.10	3.5±0.05	4.0±0.10	4.0±0.10	2.0±0.05	8.0±0.30	0.40 max.
12 mm			5.5±0.05		8.0±0.10		12.0±0.30	
16 mm (narrow SOIC)			7.5±0.10		8.0±0.10	16.0±0.30		
16 mm (wide SOIC)					12.0±0.10			

Tape-and-Reel Part Orientation



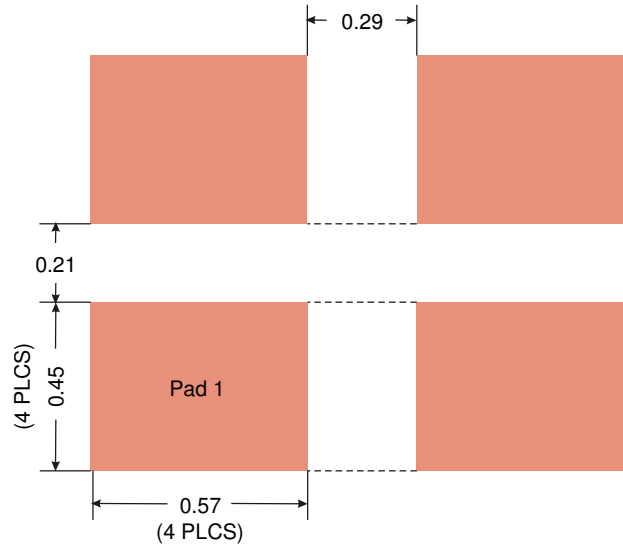
Package	Carrier Tape Width	Carrier Tape Pocket Pitch	Carrier Tape Pocket Dimensions (mm)			Standard Quantities	
			A0	B0	K0	7" Reel	13" Reel
1.1 x 1.1 mm DFN4	8 mm	4 mm	1.35 ± 0.1	1.35 ± 0.05	0.61 ± 0.05	2500	5000
2.5 x 2.5 mm DFN6	8 mm	4 mm	2.80 ± 0.1	2.80 ± 0.1	1.00 ± 0.1	2500	5000
3 x 3 mm TDFN6	12 mm	8 mm	3.30 ± 0.1	3.30 ± 0.1	1.10 ± 0.1	N/A	2500
MSOP8	12 mm	8 mm	5.30 ± 0.1	3.30 ± 0.1	1.30 ± 0.1	1000	3000
SOIC8	12 mm	8 mm	6.40 ± 0.1	5.2 ± 0.1	2.10 ± 0.1	1000	3000
QSOP16	12 mm	8 mm	6.40 ± 0.1	5.2 ± 0.1	2.10 ± 0.1	1000	3000
0.15" SOIC16	16 mm	8 mm	6.50 ± 0.1	10.30 ± 0.1	2.10 ± 0.1	800	3000
0.3" SOIC16	16 mm	12 mm	10.90 ± 0.1	10.70 ± 0.1	3.00 ± 0.1	N/A	1500

Recommended Pad Layouts

NVE recommends pad layouts with wider pad spacing than most libraries to avoid compromising isolator creepage and clearance:

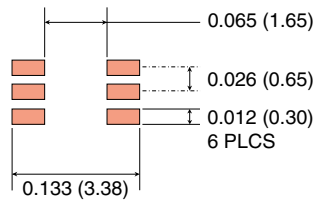
ULLGA4 Pad Layout

Dimensions in mm; scale = approx. 50x



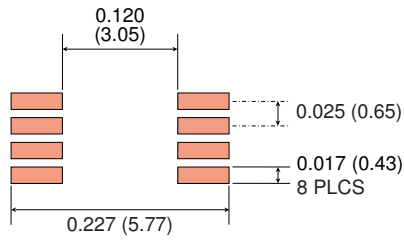
TDFN6 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



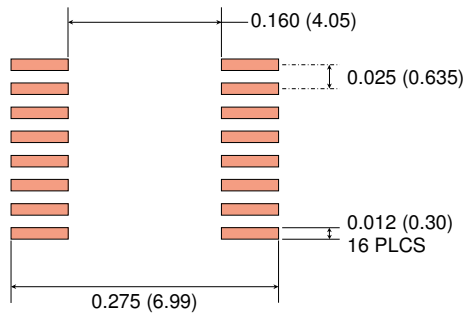
MSOP8 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



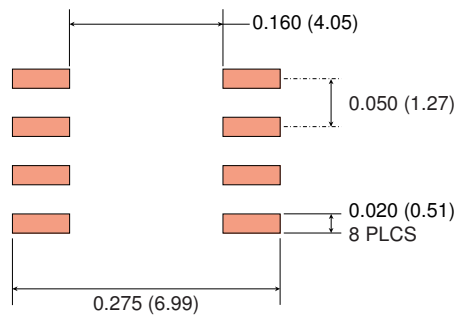
QSOP16 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



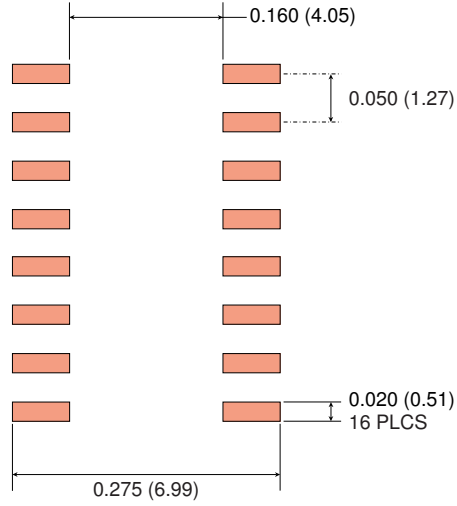
SOIC8 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



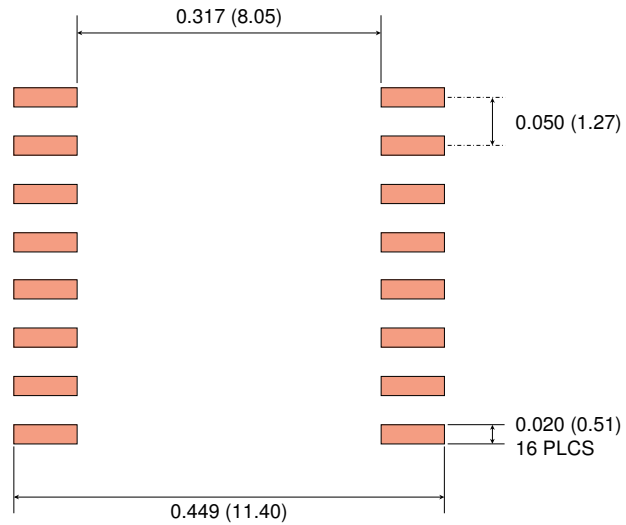
0.15" SOIC16 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



0.3" SOIC16 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



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