

Package, Tape-and-Reel and Pad Layout Specifications

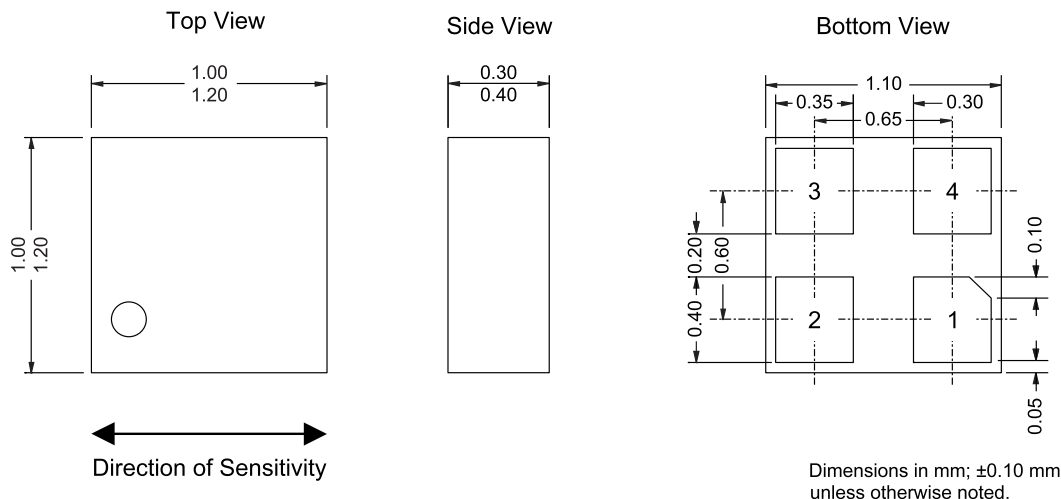
Best-in-Class Miniaturization

Package Drawings

All NVE packages are compliant with JEDEC standards, but the packages with leads also meet creepage requirements unique to isolators.

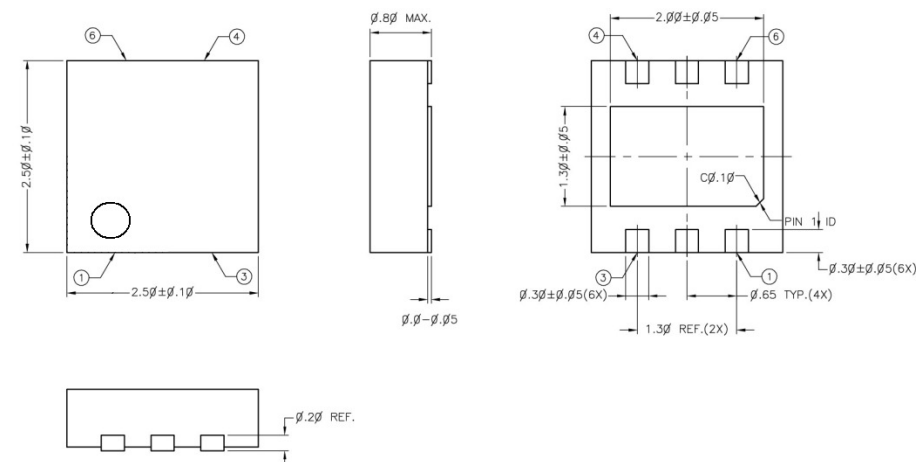
1.1 x 1.1 mm DFN4 (ULLGA)

Dimensions in mm; scale = approx. 25X



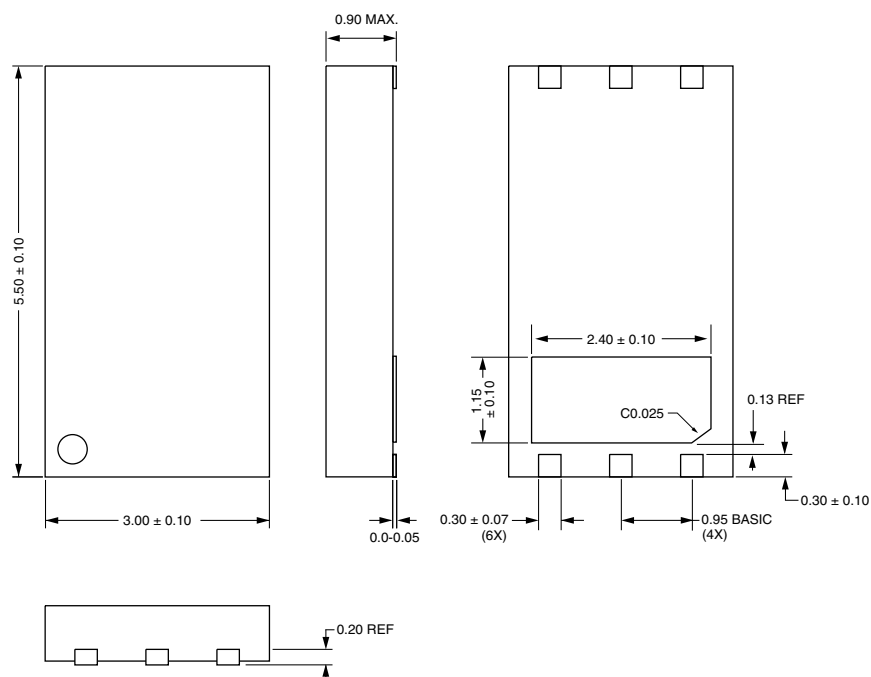
2.5 x 2.5 mm DFN6

Dimensions in mm; scale = approx. 10X



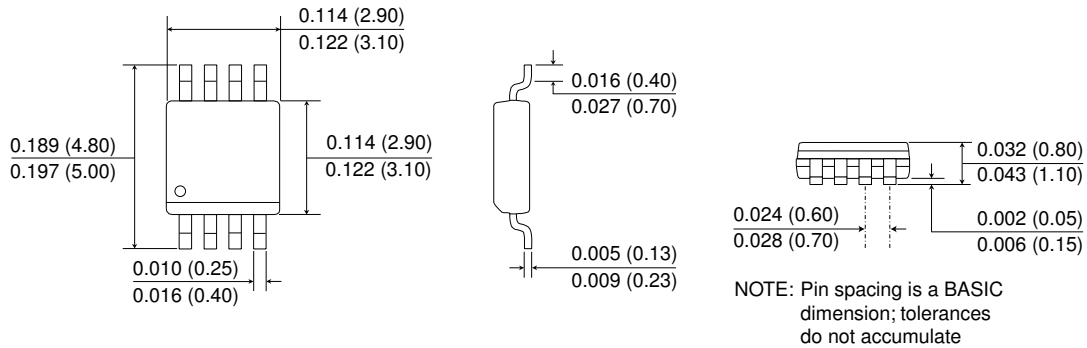
3 mm x 5.5 mm DFN6

Dimensions in mm; scale = approx. 10X



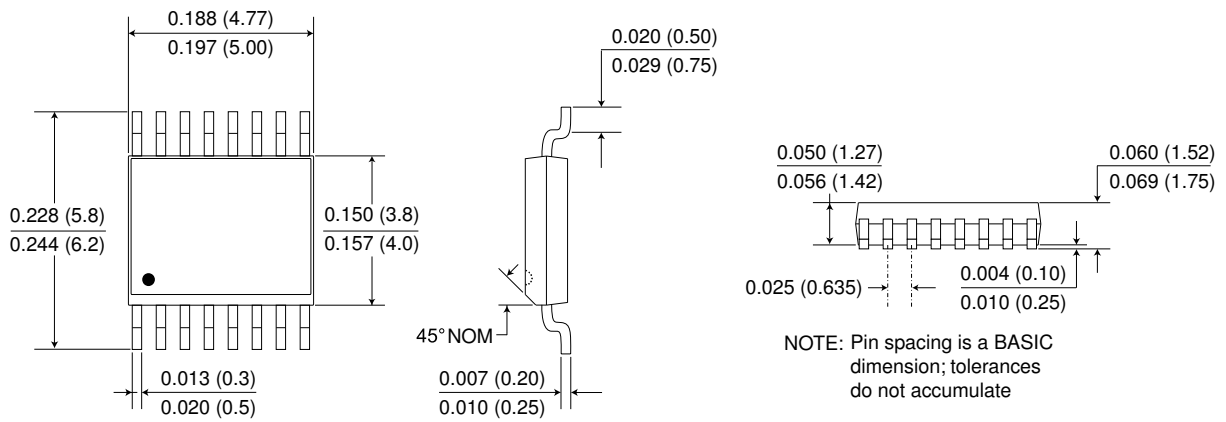
MSOP8

Dimensions in inches (mm); scale = approx. 5X



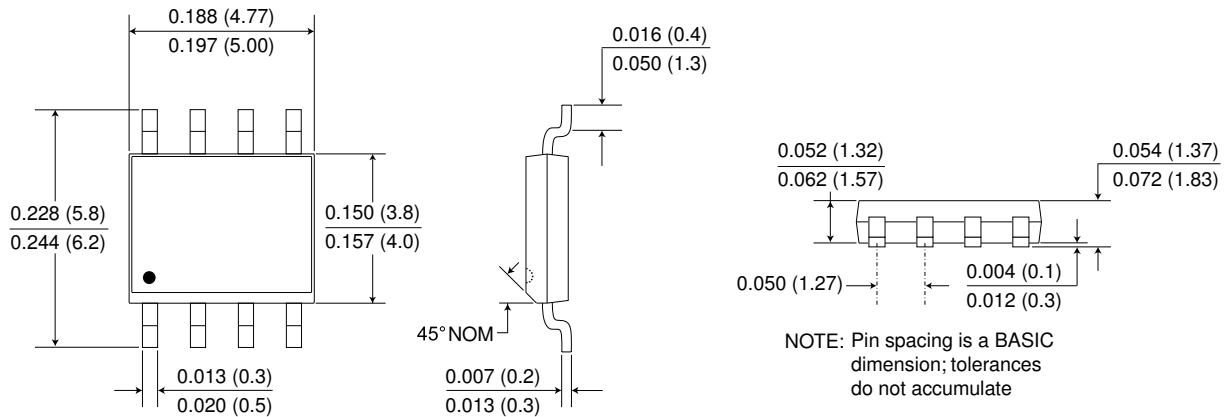
QSOP16

Dimensions in inches (mm); scale = approx. 5X



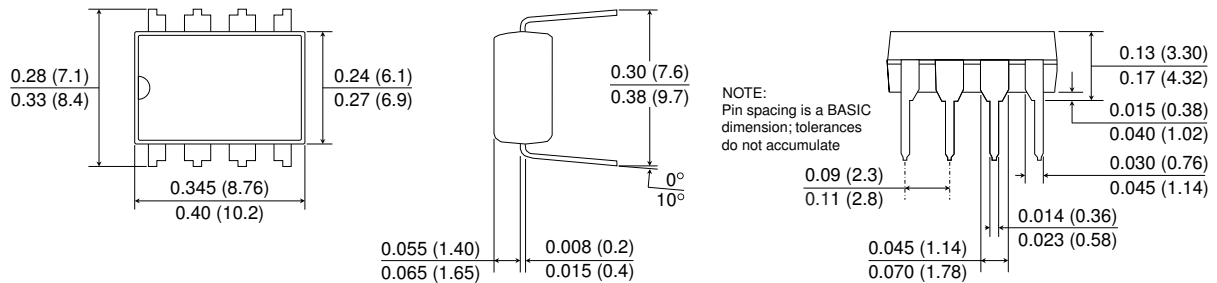
SOIC8

Dimensions in inches (mm); scale = approx. 5X



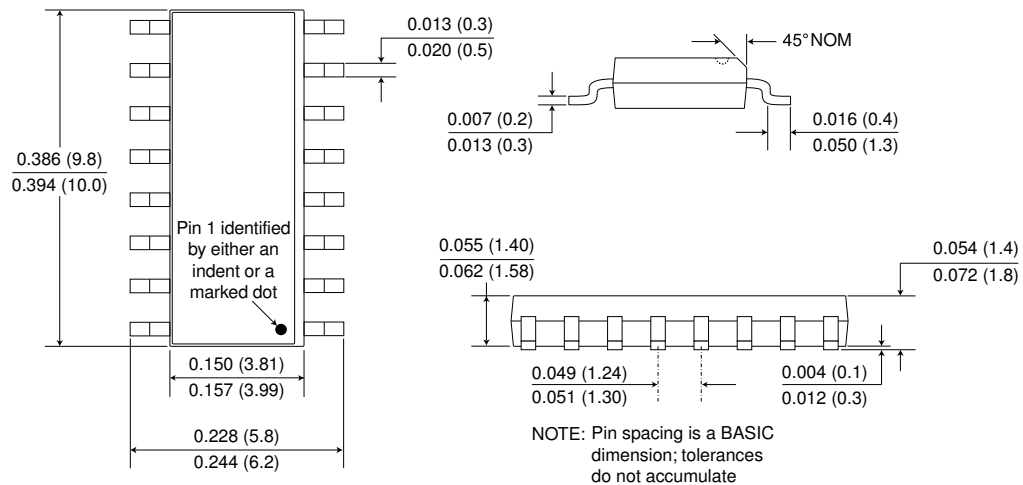
PDIP8

Dimensions in inches (mm); scale = approx. 2.5X



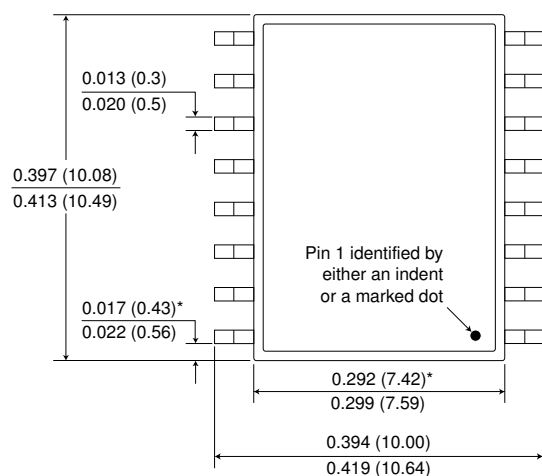
0.15" SOIC16 (SOIC16NB)

Dimensions in inches (mm); scale = approx. 5X

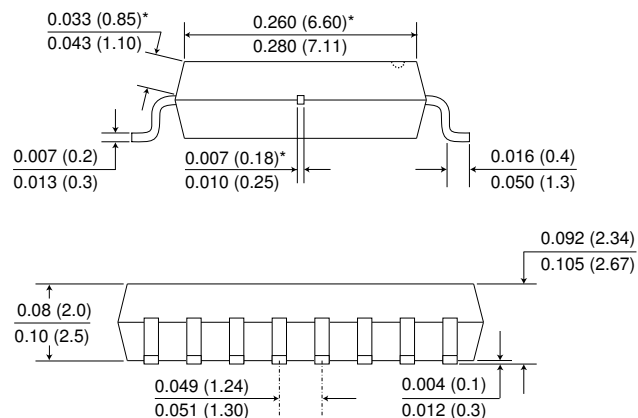


0.3" SOIC16 (SOIC16WB)

Dimensions in inches (mm); scale = approx. 5X



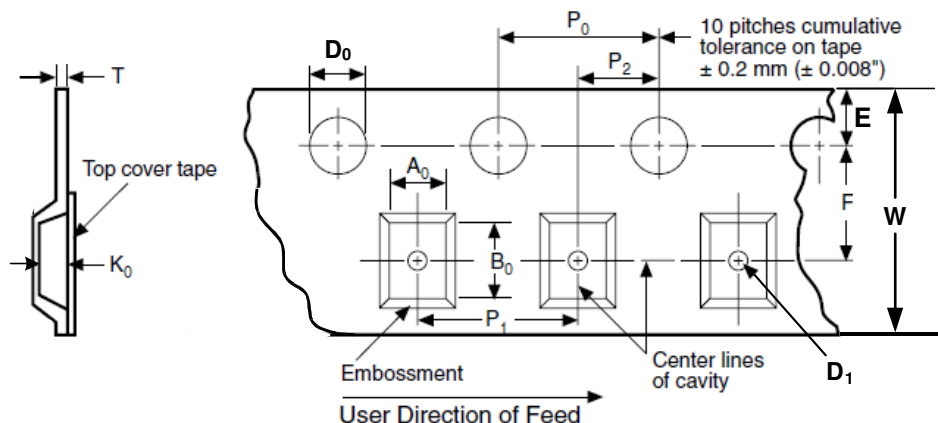
*Specified for True 8™ package to guarantee 8 mm creepage per IEC 60601.



NOTE: Pin spacing is a BASIC dimension; tolerances do not accumulate

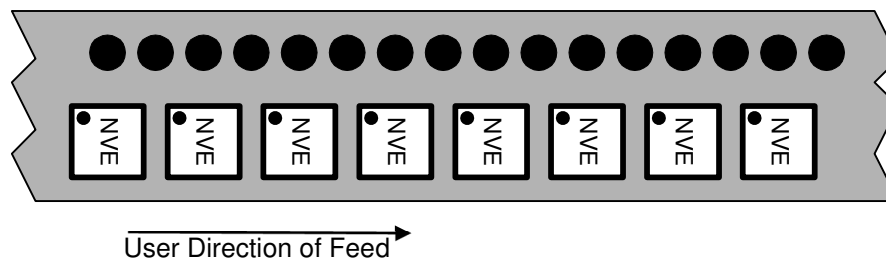
Tape-and-Reel Specifications

Tape-and-Reel Dimensions



	Dimensions in millimeters									
Tape Width	D ₀	E ₁	F	P ₀	P ₁	P ₂	W	T		
8 mm	1.5±0.1	1.75±0.1	3.50±0.05	4.0±0.1	4.0±0.1	2.0±0.05	8.0±0.3	0.4 max.		
12 mm			5.50±0.05		8.0±0.1		12.0±0.3			
16 mm (0.15" SOIC16)			7.5±0.1		8.0±0.1	2.0±0.1	16.0±0.3			
16 mm (0.3" SOIC16)					12.0±0.1					

Tape-and-Reel Part Orientation



Package	Carrier Tape Width	Carrier Tape Pocket Pitch	Carrier Tape Pocket Dimensions (mm)			Standard Quantities	
			A0	B0	K0	7" Reel	13" Reel
1.1 x 1.1 mm DFN4	8 mm	4 mm	1.35 ± 0.1	1.35 ± 0.05	0.61 ± 0.05	2500	5000
2.5 x 2.5 mm DFN6	8 mm	4 mm	2.8 ± 0.1	2.8 ± 0.1	1.0 ± 0.1	2500	5000
3 mm x 5.5 mm DFN6	12 mm	8 mm	3.3 ± 0.1	5.8 ± 0.1	1.3 ± 0.1	250	3000
MSOP8	12 mm	8 mm	5.3 ± 0.1	3.3 ± 0.1	1.3 ± 0.1	1000	3000
SOIC8	12 mm	8 mm	6.4 ± 0.1	5.2 ± 0.1	2.1 ± 0.1	1000	3000
QSOP16	12 mm	8 mm	6.4 ± 0.1	5.2 ± 0.1	2.1 ± 0.1	1000	3000
0.15" SOIC16	16 mm	8 mm	6.5 ± 0.1	10.3 ± 0.1	2.1 ± 0.1	800	3000
0.3" SOIC16	16 mm	12 mm	10.9 ± 0.1	10.7 ± 0.1	3.0 ± 0.1	N/A	1500

1.1 x 1.1 mm DFN4 Pad Layout

The diagram shows a 2x2 grid of pads. The top-left pad is labeled "Pad 1". The dimensions are as follows:

- Horizontal spacing: 0.29 (gap) and 0.57 (4 PLCS) (pad width).
- Vertical spacing: 0.21 (gap) and 0.45 (4 PLCS) (pad height).

0.251
(6.38)

0.098
(2.5)

0.047
(1.20)

0.187
(4.75)

Pin 1

0.012 (0.30)
6 PLCS

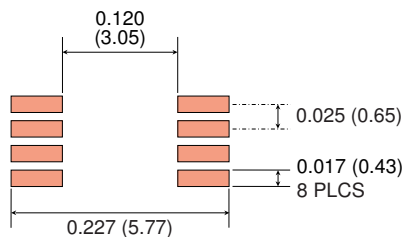
0.005 (0.13)

0.037 (0.95)
4 PLCS

Pad layouts for parts with leads are wider than most libraries to avoid compromising isolator creepage and clearance:

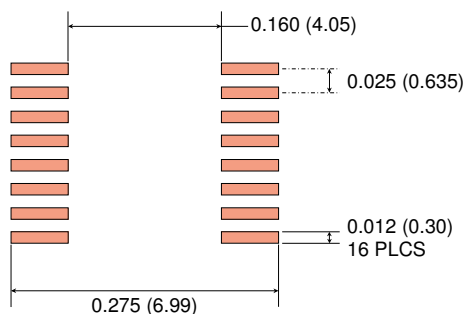
MSOP8 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



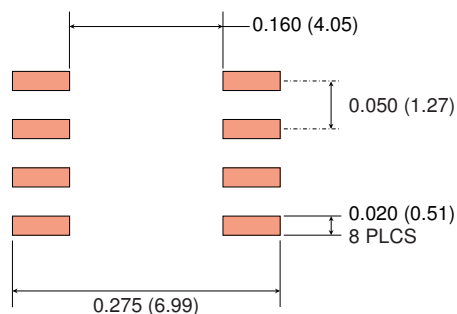
QSOP16 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



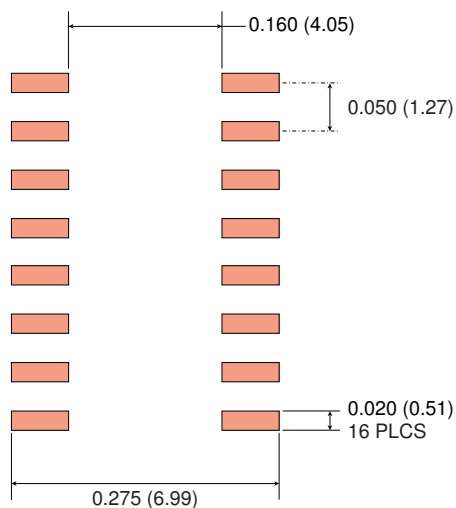
SOIC8 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



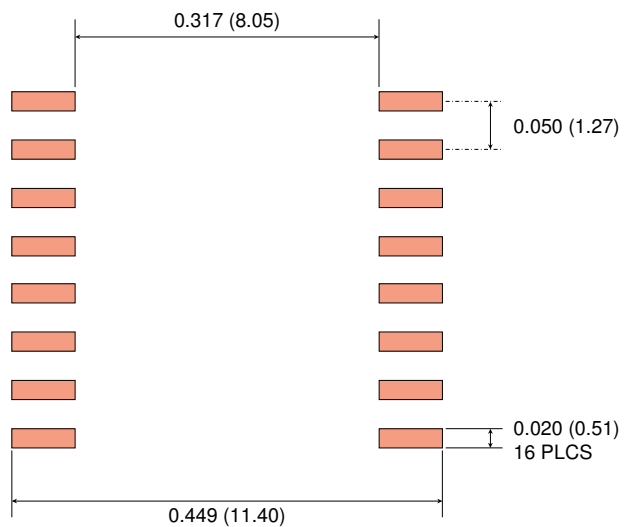
0.15" SOIC16 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



0.3" SOIC16 Pad Layout

Dimensions in inches (mm); scale = approx. 5X



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