ASR002 Smart SPI TMR Angle Sensor

Features
- Rotational speeds to 375,000 RPM
- 3.3 volt or 5 volt compatible four-wire SPI interface
- Robust airgap and misalignment tolerances
- Factory calibrated
- Ultraminiature 2.5 x 2.5 x 0.8 mm TDFN6 package

Key Specifications
- 0.1° resolution
- ±0.2° repeatability
- Robust 6 to 20 mT (60 to 200 Oe) field operating range
- Fast 12.5 kSps sample rate
- Flexible 2.2 to 3.6 V supply range
- Low 4 mA typical supply current
- Full −40 °C to 125 °C operating range

Applications
- Rotary encoders
- Robotics
- Motor control
- Automotive applications
- Internet of Things (IoT) end nodes

Description
ASR002 TMR Smart Angle Sensors provide a precise digital angle measurement over a wide range of speeds.

The sensor combines precise, low-power Tunneling Magnetoresistance (TMR) sensing elements with sophisticated digital signal processing.

The sensor is factory calibrated, with coefficients stored in internal memory.

A four-wire SPI interface provides angle data and allows setting device parameters.

The ASR002 is designed for harsh industrial or automotive environments with ESD protection, and full −40 °C to 125 °C operating temperature range.
### Boundary Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min.</th>
<th>Max.</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply voltage</td>
<td>−12</td>
<td>4.2</td>
<td>Volts</td>
</tr>
<tr>
<td>Input and output voltages (MISO, MOSI, SS, SCLK)</td>
<td>−0.5</td>
<td>Vcc+2.5 up to 5.8</td>
<td>Volts</td>
</tr>
<tr>
<td>Input current</td>
<td>−100</td>
<td>+100</td>
<td>mA</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>−55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>ESD (Human Body Model)</td>
<td></td>
<td>2000</td>
<td>Volts</td>
</tr>
<tr>
<td>Applied magnetic field</td>
<td></td>
<td>Unlimited</td>
<td>Tesla</td>
</tr>
</tbody>
</table>
## Operating Specifications (T_{\text{min}}; T_{\text{max}}; 2.2 < V_{\text{DD}} < 3.6 \text{ V})

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Units</th>
<th>Test Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating temperature</td>
<td>T_{\text{min}}; T_{\text{max}}</td>
<td>-40</td>
<td></td>
<td>125</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Supply voltage</td>
<td>V_{\text{DD}}</td>
<td>2.2</td>
<td></td>
<td>3.6</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Supply current</td>
<td>I_{\text{DD}}</td>
<td>4</td>
<td>6</td>
<td></td>
<td>mA</td>
<td>Max. at V_{\text{DD}} = 3.6V</td>
</tr>
<tr>
<td>Power-on Reset supply voltage</td>
<td>V_{\text{POR}}</td>
<td>1.4</td>
<td></td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Brown-out power supply voltage</td>
<td>V_{\text{BOR}}</td>
<td>0.75</td>
<td>1</td>
<td>1.36</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Start-up time</td>
<td>T_{\text{STU}}</td>
<td>15</td>
<td></td>
<td></td>
<td>ms</td>
<td></td>
</tr>
</tbody>
</table>

### Magnetics

| Applied magnetic field strength             | H      | 6     | 12    | 20    | mA    |                |
| LDMN                                         |        | 60    | 120   | 200   | Oe    |                |

### Accuracy and Repeatability

| Angular resolution                         | δ      | 0.1   |       |       | Degrees       | Fixed temperature and bias¹ |
| Angular hysteresis                         | δ̂     | 0.1   |       |       | Degrees       | 0 to 85°C                    |
| Repeatability                              | ±0.2   |       |       |       |                | −40 to 125°C                 |
| Angular accuracy, fixed bias¹              | ε      | ±2    | ±3    | ±6    | Degrees       | −40 to 125°C                 |

### Speed

| Sample rate                                | 12.5 kSPs |

### SPI Bus Characteristics

| Bus voltage                                | V_{\text{BUS}} | 2.2   | 5.5   | V     |                |
| Low level input threshold voltage          | V_{\text{IL}}  | 0.8   |       | V     |                |
| High level input threshold voltage         | V_{\text{IH}}  | 2.2   |       | V     |                |
| Low level output current                   | I_{\text{OD}}  | 3     |       | mA    | V_\text{CE} = 0.4V |
| I/O capacitance                           | C_{\text{IO}} | 10 pF |       |       |                |

### SPI Setup and Hold Timing

| Data transfer rate                         | DR     | 2     | Mbits/s | Full duplex          |
| SCLK Rise time                             | t_{\text{R}} |       | ns      |                      |
| SCLK fall time                             | t_{\text{F}} |       | ns      |                      |
| SCLK low time                              | t_{\text{CL}} | 200   | ns      |                      |
| SCLK high time                             | t_{\text{CH}} | 200   | ns      |                      |
| SS to SCLK setup                           | t_{\text{SE}} |       | ns      |                      |
| SCLK to MISO valid                         | t_{\text{SDD}} | 170   | ns      |                      |
| SS to MISO tri-state                       | t_{\text{SDZ}} | 170   | ns      |                      |
| SCLK to MOSI hold time                     | t_{\text{SDH}} | 80    | ns      |                      |
| MOSI to SCLK setup                         | t_{\text{SDS}} | 80    | ns      |                      |
| SCLK to SS hold time                       | t_{\text{SH}}  | 80    | ns      |                      |
| SS to MOSI valid                           | t_{\text{SEZ}} | 170   | ns      |                      |

### RAM Timing

| Address setup time                         | t_{\text{ADDR}} | 3      | µs      | See figure 4      |
| Data read time                             | t_{\text{READ}} | 10     | µs      | See figure 4      |

### Nonvolatile Memory Characteristics

| Address setup time                         | t_{\text{ADDR}} | 3      | µs      | See figure 5      |
| Data read time                             | t_{\text{READ}} | 10     | µs      | See figure 5      |
| Data write time                            | t_{\text{NVM}}  | 20     | ms      |                    |
| Endurance                                  | 10000           |       | Cycles  |                    |

### Package Thermal Characteristics

| Junction-to-ambient thermal resistance     | θ_{JA} | 320 | °C/W    |                    |
| Package power dissipation                 |       | 500 | mW      |                    |
Specification Notes:
1. “Fixed Bias” means a fixed airgap within between the bias magnet and sensor so the magnitude of the magnetic field at the sensor is constant within the specified field range of the parts. The highest accuracy is obtained using fields closest to the 17.5 mT (175 Oe) factory calibration field.

2. “Variable Bias” means the magnitude of the magnetic field at the sensor can vary across the entire specification range.
ASR002 Overview
The ASR002 is a non-contact angle sensor designed for high speed applications where size is limited. The heart of the ASR002 is a tunneling magnetoresistive (TMR) sensor. In a typical configuration, an external magnet provides a magnetic field of 6 to 20 mT (60 to 200 Oe) in the plane of the sensor, as illustrated below for a bar magnet and a diametrically-magnetized disk magnet. Factory-programmed signal conditioning is combined with a temperature sensor and digital linearization to produce high speed, accuracy, and precision in a tiny 2.5 x 2.5 mm TDFN package.

ASR002 Operation
A detailed block diagram is shown below:

![Block Diagram](image)

**TMR Angle Sensor Element**
ASR002 sensors use unique TMR sensor elements that are inherently high speed and low noise. The digital core calculates the angle from sensor element Sine and Cosine vectors, and the raw sensor data are available from separate memory locations.

**ADC**
The sensor output is digitized with a 12-bit ADC. The extra bits ensure precision and computational accuracy.

**Digital Filter**
A first-order Infinite Impulse Response (IIR) digital filter with a programmable cutoff frequency can be used for ultralow noise if high-frequency operation is required. The factory default is the filter turned off.
Rotation Direction
The ASR002 can provide increasing angle values for either clockwise or counterclockwise field rotations. Counterclockwise is defined as a rotating field vector through pins 1-3-4-6, and clockwise through pins 1-6-4-3. The rotation direction can be programmed using the $\theta_{DIR}$ parameter.

Figure 3. Zero-angle reference ($\theta_0$) and rotation direction ($\theta_{DIR}$). The rotational center of the sensor is the package center.

Zero-Angle Reference Point
A programmable parameter $\theta_0$ sets the zero-degree reference or angular offset. This is the angle of “discontinuity,” that is, where the angle output changes from 360° to 0°. The default $\theta_0$ value is zero for magnetic fields pointing from pin 1 to pin 6.

Direction and Hysteresis
The Direction output indicates direction of rotation. A hysteresis setting can be changed to prevent small changes from causing the Direction output to “chatter,” especially at low speed.

Digital Filter
The digital filter is an Infinite impulse response (IIR) weighted running average filter, which can reduce mechanical and electrical noise depending on the required speed.

The filtered output is calculated as follows:

$$\theta_n = \theta / m + [(m-1)/m] \theta_{n-1}$$

Where $\theta$ is the measured angle; $\theta_n$ is the filtered angle; $\theta_{n-1}$ is the previous value of the filtered angle; and $m$ is a constant that determines the cutoff frequency as follows:

$$f_{CUTOFF} = f_{SAMPLE} / (2\pi m)$$

Where $f_{CUTOFF}$ is the filter cutoff frequency and $f_{SAMPLE}$ is the sensor ADC sampling rate (approximately 12500/s). So for example, if $m = 10$, the cutoff frequency is approximately 200 Hz.

$m = 1$ disables filter so the output is simply updated with each sample.

A Simple SPI Interface
The SPI interface is an industry standard four-wire, full-duplex 2 megabit per second connection with the sensor as the slave to an external master such as a microcontroller. SPI data (MOSI and MIS0) and the Clock (SCLK) are 2.2 volt to five-volt compliant. The digital angle is the default two byte response.

The ASR002 uses an industry-standard “Mode 0” interface (data is sampled at the leading rising edge of the clock; CPOL=0 and CPHA=0). In accordance with industry standards, slave select (SS) is active-low, and bit order and byte order are from MSB to LSB.

Details are shown in the following timing diagrams:
Figure 4a. Sending the address for a read.

Figure 4b. Reading data.

Figure 5a. Sending the address for a write.

Figure 5b. Writing data.

Figure 6. Continuous read.
SPI setup and hold timing constraints are shown in Figure 7:

![Figure 7. SPI setup and hold timing.](image)

A schematic of a typical interface to a 3.3-volt or five-volt microcontroller is shown in Figure 10.

**Straightforward Reading and Writing**

The sensor is reset on a falling edge of SS. All reads and writes are initiated by the master pulling SS “LOW” and sending an eight-bit address to the ASR002 plus a second byte. The least significant bit of the second address byte indicates whether the address request is for a read or a write (“0” is a read; “1” is a write). The slave responds with two bytes of data.

As shown in figures 4 and 5, and the specification table, a 3 µs delay (tADDR) is needed between address bytes; 10 µs (tREAD) should be allowed before data can be read, and 20 ms (tNVM) should be allowed for writing parameters to the nonvolatile memory.

**Reading the angle**

To read the angle, the master simply writes two zero bytes for the “0” angle address, then reads the two-byte angle, which is expressed in tenths of degrees. These two-byte reads can be repeated to continuously read the angle as shown in the Figure 6 timing diagram and the code on p. 14.

**Reading and writing parameters**

Reading and writing parameters are simple four-byte sequences. The master writes two bytes for the parameter address, then reads or writes two bytes for the parameter value. Illustrative code to zero the sensor by writing the offset parameter is shown on p. 15.

The number of bits in different parameters varies. Unused bits are sent as zeros by the sensor. Similarly, unused bits should be written as zeros to the sensor to avoid an out-of-range parameter that could be ignored.

Because of the slower speed of the sensor’s nonvolatile memory, allow 15 ms for parameter writes.
Memory Map

The ASR002 memory provides access to angle data and user-programmable parameters. The memory is accessed via SPI as described in the SPI interface section.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Default</th>
<th>Read/Write</th>
<th>Range</th>
<th>Address</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Data</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Angle</td>
<td>θ</td>
<td>N/A</td>
<td>R</td>
<td>0 – 3600</td>
<td>0x00</td>
<td>In tenths of a degree</td>
</tr>
<tr>
<td>Raw Sin Vector</td>
<td>Sinθ</td>
<td>N/A</td>
<td>R</td>
<td>Approx.</td>
<td>0x01</td>
<td>Raw outputs centered at approx. 2048 with peak-peak amplitudes of approx. 1000.</td>
</tr>
<tr>
<td>Raw Cos Vector</td>
<td>Cosθ</td>
<td>N/A</td>
<td>R</td>
<td>1500 – 2500</td>
<td>0x02</td>
<td></td>
</tr>
<tr>
<td>Direction</td>
<td>Dir</td>
<td>0 – 1</td>
<td>R</td>
<td>0x03</td>
<td>0 = decreasing angle; 1 = increasing angle</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>User-Programmable Parameters</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rotation Direction</td>
</tr>
<tr>
<td>Angular Offset</td>
</tr>
<tr>
<td>Digital Filter Constant</td>
</tr>
<tr>
<td>Direction Hysteresis</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Read-Only Memory</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lot code</td>
</tr>
<tr>
<td>WW</td>
</tr>
<tr>
<td>XX</td>
</tr>
</tbody>
</table>

Table 1. ASR002 Memory Locations.
Power-Up and Initialization

**Absolute position**
Unlike some encoder types, ASR002 sensors detect absolute position and maintain position information when the power is removed. The sensor powers up indicating the correct position.

**Nonvolatile parameters**
All parameters are nonvolatile so they can be set once (via SPI), and remain for the life of the product if desired.

**Minimizing Noise**
Several steps minimize noise:
- A 10 µF bypass capacitor is recommended as close as possible to the V_{DD} and GND pins. A 0.080 x 0.050 inch or smaller capacitor is recommended to minimize magnetic interference with the sensor.
- Use a circuit board ground plane.
- Grounding the sensor’s center pad allows the leadframe to act as a shield.
Magnet Selection

The sensor’s wide operating field range of 6 to 20 mT (60 to 200 Oe) allows inexpensive magnets and operation over a wide range of magnet spacing. The figures below show the magnetic field for various magnet geometries and distances for inexpensive C5/Y25 grade ferrite magnets:

![Figure 8. Magnetic fields for various geometries of C5/Y25 ferrite magnets plotted for the distance between the magnet and sensor. Eight-millimeter diameter magnets of various thicknesses are shown at left, and four-millimeter thick magnets of various diameters are shown at right.](image1)

Field varies less with distance for larger magnets, so maximizing magnet size within the mechanical constraints of the system maximizes accuracy.

Higher-grade magnets can be used for high-temperature applications or large magnet-sensor separations. The graph below shows field strengths with various materials:

![Figure 9. Magnetic fields from an 8 millimeter diameter, 4 millimeter thick magnet for increasing magnet-sensor separation. NdFeB materials produce the largest magnetic fields and separations. SmCo and AlNiCo materials offer the highest operating temperatures. Ferrite magnets are the most cost-effective.](image2)

Our free Web app can be used to determine optimum separations for various magnet sizes and materials: [https://www.nve.com/spec/calculators.php](https://www.nve.com/spec/calculators.php).

NVE’s Online Store stocks popular magnets.
The ASR002 is configured as a Slave and the microcontroller should be configured as the Master. The ASR002 SPI interface is compatible with 3.3 or five-volt microcontrollers.
Typical Read and Write Communications Pseudocode

//SPI clock set elsewhere (2 MHz max.)
//SPSR = SPI Status Register; SPIF = SPI Status Register Interrupt flag
//SS set low (active) elsewhere
{
    case COMM_GET_MEM: //Routine to READ memory
        SPDR=buffer[1]; //Sends the address to read from
        while(! (SPSR & (1<<SPIF))); //Waits for transmission
        _delay_us(3); //Allow 3 microseconds between address bytes
        SPDR=0x00; //'0' for second address byte (indicates read)
        while(! (SPSR & (1<<SPIF))); //Waits for transmission
        _delay_us(10); //Allows 10 microseconds for the address to be sent
        SPDR=0x00;
        while(! (SPSR & (1<<SPIF))); //Waits for transmission to complete
        _delay_us(10); //Allows 10 microseconds for data to be sent
        MSB=SPDR; //Reads the first byte of data (MSB)
        SPDR=0x00;
        while(! (SPSR & (1<<SPIF))); //Wait for transmission to be complete
        _delay_us(10); //Allow address byte to be sent
        LSB=SPDR; //Reads the second byte of data (LSB)
        buffer[0]=MSB;
        buffer[1]=LSB;
        *output_len=2; //Number of bytes to transmit
        break;
    }
    case COMM_SET_MEM: //WRITE memory routine (to set sensor parameters)
        SPDR=buffer[1]; //Puts the address to read from in the buffer
        while(! (SPSR & (1<<SPIF))); //Wait for transmission to be complete
        _delay_us(3); //Allow address byte to be sent
        SPDR=0x01; //1' for second address byte (write bit)
        while(! (SPSR & (1<<SPIF))); //Wait for transmission to complete
        _delay_us(10); //Allows time for data to be sent
        SPDR=buffer[2]; //Read first data byte (MSB)
        while(! (SPSR & (1<<SPIF))); // Allows time for the data to be sent
        _delay_us(10);
        SPDR=buffer[3]; //Read second data byte (LSB)
        while(! (SPSR & (1<<SPIF))); // Allows 20 MILLISecseconds to write to nonvolatile memory
        _delay_ms(20);
        break;
}
Illustrative Arduino Code for Continuous Read

/***********************************************************/
Continuously read the angle from an NVE ASR002 Smart Angle Sensor
Arduino Uno connections: pin 10=SS; pin 11=MOSI; pin 12=MISO; pin 13=SCLK
/***********************************************************/

#include <SPI.h>

int angle;

void setup() {
  pinMode(10, OUTPUT); //Pin 10 = Sensor SS
  SPI.begin();

  //Set clock rate at 2 Mbits/s; MSB first; Mode 0
  SPI.beginTransaction(SPISettings(2000000, MSBFIRST, SPI_MODE0));
  digitalWrite(CS, HIGH); //Disable to reset the sensor
  digitalWrite(10, LOW); //Re-enable sensor
}

void loop() {
  angle = (SPI.transfer (0))<<8; //Send 0 for address angle; receive angle MSB
  delayMicroseconds (3); //Allow 3 us between address bytes
  angle |= SPI.transfer (0); //2nd address byte (0 for read); receive angle LSB
  delayMicroseconds (10); //Allow 10 us for next data
}
Illustrative Arduino Code to Zero the Sensor

 Zeros an ASR002 at its current location to establish a "home position." Arduino Uno connections: pin 11=MOSI; pin 12=MISO; pin 13=SCLK; pins 9 & 10=SS
Includes a simple procedure to read the angle.

```c
#include <SPI.h>

int angle;

void setup() {
  pinMode(10, OUTPUT); //Pin 10 = Sensor SS
  SPI.begin();
  //Set clock rate to 2 Mbits/s; MSB first; Mode 0
  SPI.beginTransaction(SPISettings(2000000, MSBFIRST, SPI_MODE0));
  digitalWrite(10, HIGH); //Disable to reset the sensor
  digitalWrite(10, LOW); //Re-enable sensor

  //Read starting angle
  SPI.transfer(0); //Send 0 for address angle to read starting angle
  delayMicroseconds (3); //Allow 3 us between address bytes
  angle = (SPI.transfer (0x41))<<8; //Read starting angle MSB and point to offset address
  delayMicroseconds (3); //Allow 3 us between address bytes
  angle |= SPI.transfer (1); //Read angle LSB; 2nd address byte = 1 for write
  delayMicroseconds (10); //Allow 10 us for next data

  //Reset offset
  SPI.transfer (0); //Write offset MSB
  delayMicroseconds (3); //Allow time between bytes
  SPI.transfer (0); //Write offset LSB
  delay (20); //20 ms NVM delay

  //Write measured angle to offset parameter to zero the sensor
  angle=getAngle(); //Read angle now that offset has been set to zero
  SPI.transfer (0x41); //Point to offset address
  delayMicroseconds (3); //Allow time between address bytes
  SPI.transfer (1); //2nd address byte = 1 for write
  delayMicroseconds (10);
  SPI.transfer (0x41); //Write to offset parameter to zero the sensor
  delayMicroseconds (3);
  SPI.transfer (angle & 0xFF); //Write LSB
  delay (20); //20 ms NVM delay
}

void loop() {} 

//Procedure to read the angle
int getAngle() {
  int angle;
  angle = (SPI.transfer (0))<<8; //Send 0 for address angle; receive angle MSB
  delayMicroseconds (3); //Allow 3 us between address bytes
  angle |= SPI.transfer (0); //2nd address byte (0 for read); receive angle LSB
  delayMicroseconds (10); //Allow 10 us for next data
  return angle;
}
```
In Case of Difficulty

Random data, or measured angles outside the allowable 0 to 3600 range.
- The SPI clock may be too fast (the ASR002 maximum clock rate is specified as 2 Mbits/s).
- Ensure the Master is operating in the correct mode (Mode 0).

Random data, or measured angles outside the allowable 0 to 3600 range on the first readings after the sensor is selected.
- The sensor is reset on a falling edge of SS. Toggling SS HIGH, then LOW will ensure the sensor is reset.

MSB/LSB bytes are reversed.
- The MSB should be read first. SPI devices use different byte orders, but the ASR002 follows the most common convention of MSB first.

Angle data is shifted by one or more bits.
- This is usually because the sensor has not completed internal shifting of bits into the correct positions. Ensure there is enough settling time between writing the address and reading the data (10 µs minimum).

Garbled data on first startup of Master.
- Data can be left in the sensor if the Master microcontroller is reset and the sensor is not. This can be corrected by doing a “dummy read” as part of the microcontroller startup sequence, or toggling SS HIGH then LOW to reset the sensor.

Parameters do not appear to be written correctly.
- Ensure that the Write bit is set in the second (LSB), i.e., the second address byte is a “1.”
- Ensure there is adequate settling time before reading or using a written parameter (10 milliseconds minimum). Parameters are stored in nonvolatile memory, not RAM, and writing to nonvolatile memory is much slower.
Evaluation Support

Breakout Board
The AG957-07E breakout board provides easy connections to an ASR002-10E angle sensor with a six pin connector. It also has a recommended 10 µF bypass capacitor:

![Figure 11. AG957-07E breakout board (actual size)
0.5" x 0.6" (12 mm x 15 mm)](image)

Smart Angle Sensor Evaluation Kit
This simple board includes an ASR002-10E Smart Angle Sensor, a microcontroller that interfaces to the Sensor via SPI, and to a PC via USB. The kit includes a diametrically-magnetized cylindrical horseshoe magnet and fixturing. A PC-based user interface provides two-way communication with the sensor to display the sensor outputs and change the sensor’s parameters.

![Figure 12. AG956-07E: Smart Angle Sensor Evaluation Kit.](image)

Socket Board
The AG954-07E provides a TDFN6 socket for easy interface to sensors such as the ASR002-10E without soldering:
Figure 13. AG954-07E: TDFN socket board
1.5” x 2” (38 mm x 50 mm)(actual size)
Magnets
NVE stocks five popular magnets for use with its angle sensors:

<table>
<thead>
<tr>
<th>NVE Part Number</th>
<th>Compatible Magnet Holder</th>
<th>Diameter (mm)</th>
<th>Length (mm)</th>
<th>Typ. sensor distance (mm; 12 mT/120 Oe nom. field)</th>
<th>Material and Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>12526</td>
<td>4 mm</td>
<td>4</td>
<td>4</td>
<td>3</td>
<td>C5/Y25 ferrite disk magnets</td>
</tr>
<tr>
<td>12249</td>
<td>N/A</td>
<td>12.5</td>
<td>3.5</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>12527</td>
<td>8 mm</td>
<td>8</td>
<td>4</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>12528</td>
<td>8 mm</td>
<td>8</td>
<td>8</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>12426*</td>
<td>N/A</td>
<td>11</td>
<td>11</td>
<td>8</td>
<td>Alnico-5 round horseshoe magnet with mounting hole</td>
</tr>
</tbody>
</table>

*Included in the AG956-07E Smart Angle Sensor Evaluation Kit.

Table 2. Popular magnets for angle sensing.

Magnet Holders
NVE offers two magnet holders for evaluation and prototyping. The holders are machined aluminum. Set screws secure the magnets in the holders and allow magnet position adjustments. There are threaded mounting holes for a thumbscrew to turn the magnet, or the hole can be used to attach the holder to a rotating shaft. A “clockhand” indicator helps track magnet rotation:

![Magnet Holders Diagram]

Figure 14. 4 mm magnet holder (part #12546; left) and 8 mm magnet holder (part #12545; right). 0.44" dia. x 0.88" tall (11 mm x 22 mm) outside dimensions; actual size.

The holders are compatible with several popular diametrically-magnetized disk magnets and can be used in the AG956-07E Evaluation Kit:

<table>
<thead>
<tr>
<th>Holder Part Number</th>
<th>Outside Dimensions</th>
<th>Compatible Magnets (NVE part #s)</th>
<th>Magnet Diameter (mm)</th>
<th>Max. Magnet Length (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>12546</td>
<td>11 mm dia. x</td>
<td>12526</td>
<td>4</td>
<td>4</td>
</tr>
<tr>
<td>12545</td>
<td>22 mm tall</td>
<td>12527; 12528</td>
<td>8</td>
<td>8</td>
</tr>
</tbody>
</table>

Table 3. Magnet holders.
**Smart TMR Angle Sensor**

**2.5 x 2.5 mm TDFN6 Package**

![Diagram of the 2.5 x 2.5 mm TDFN6 Package]

<table>
<thead>
<tr>
<th>Pad</th>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>GND</td>
<td>Ground/VSS</td>
</tr>
<tr>
<td>2</td>
<td>SCLK</td>
<td>SPI Clock Input</td>
</tr>
<tr>
<td>3</td>
<td>MISO</td>
<td>Sensor SPI Data Output</td>
</tr>
<tr>
<td>4</td>
<td>VDD</td>
<td>Power Supply (bypass with a 10 μF capacitor)</td>
</tr>
<tr>
<td>5</td>
<td>MOSI</td>
<td>Sensor SPI Data Input</td>
</tr>
<tr>
<td>6</td>
<td>SS</td>
<td>Sensor Select Input (low to select)</td>
</tr>
</tbody>
</table>

Center pad
- Internal leadframe connection; connect to GND to minimize noise.

**Notes:**
- Dimensions in millimeters.
- Soldering profile per JEDEC J-STD-020C, MSL 1.
Ordering Information

ASR002 - 10E TR13

Product Family
ASR = Smart Angle Sensors

I/O
00 = SPI
01 = I²C / PWM
01 = ABZ (Encoder)

Sensor Element
2 = High speed, medium accuracy

Field Range Identifier
Blank = General Purpose (6 to 20 mT / 60 to 200 Oe)

Part Package
10E = RoHS-Compliant 2.5 x 2.5 mm TDFN6 Package

Bulk Packaging
TR13 = 13” Tape and Reel Package

Available Product Variants

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Breakout Board</th>
<th>Evaluation Kit</th>
<th>Repeat-ability</th>
<th>Resolution</th>
<th>Speed</th>
<th>Outputs</th>
</tr>
</thead>
<tbody>
<tr>
<td>ASR002-10E</td>
<td>AG956-07</td>
<td>AG956-07</td>
<td>0.2°</td>
<td>0.1°</td>
<td>12500 Sps</td>
<td>SPI</td>
</tr>
<tr>
<td>ASR012-10E</td>
<td>AG966-07</td>
<td>AG963-07</td>
<td>512 virtual lines (128 cycles) / rev.</td>
<td>I²C; PWM</td>
<td></td>
<td></td>
</tr>
<tr>
<td>ASR022-10E</td>
<td>AG967-07</td>
<td>AG964-07</td>
<td>ABZ; Dir</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
## Revision History

<table>
<thead>
<tr>
<th>Revision</th>
<th>Date</th>
<th>Changes</th>
</tr>
</thead>
</table>
| SB-00-081-K | September 2019 | - Recommend 10 µF bypass capacitor added specifics on p. 10.  
- Added bypass capacitor details to microcontroller application circuit. (p. 12).  
- Added second bypass capacitor to breakout board (p. 17). |
| SB-00-081-J | August 2019   | - Added Absolute Maximum output current specification (p. 2).  
- Offering a breakout board instead of a bare board (p. 17). |
| SB-00-081-I | July 2019     | - Clarified lot code formatting and corrected its memory address range (p. 9). |
| SB-00-081-H | June 2019     | - Noted 17.5 mT (175 Oe) factory calibration field (p. 4, Note 1).  
- Clarified two-byte and four-byte SPI read and write sequences (p. 8).  
- Corrected default filter memory setting (p. 9). |
| SB-00-081-G | June 2019     | - Added SI units (tesla) in addition to CGS (oersteds).  
- Changed Operating Specification “RAM Timing” from maximums to minimums. |
| SB-00-081-F | April 2019    | - Recommend 1 µF bypass capacitor. |
| SB-00-081-E | Feb. 2019     | - Improved “Magnet Selection” section.  
- Added magnet and magnet holder information (p. 18). |
| SB-00-081-D | Feb. 2019     | - Added details on center pad and grounding recommendation to minimize noise. |
| SB-00-081-C | Jan. 2019     | - Faster RAM timing.  
- Reduced data transfer rate from 2.5 to 2 Mbits/s for more design margin.  
- Corrected number of bits in angular offset.  
- Added Arduino code to zero the Sensor.  
- Added “In Case of Difficulty” section.  
- Dropped daisy-chained SPI application diagram (not supported).  
- Added AG954-07E socket board.  
- Typographic and cosmetic changes. |
| SB-00-081-B | Jan. 2019     | - Added typical communications pseudocode. |
- Added detailed SPI timing diagrams.  
- Tightened typ. supply current specification to 4 mA.  
- Revised minimum operating field to 60 Oe.  
- Added sensor direction output and hysteresis parameter.  
- Added raw Sin and Cos vector outputs.  
- Added illustrative microcontroller code.  
- Finalized pinout.  
- Added evaluation kit and board.  
- Dropped customer calibration capability (unnecessary).  
- Various typographic corrections. |
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